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Application#	Patent#	Status	Date Filed	Title	Inventor Name 2
10680192	Not Issued	019	01/01/0001	DATA BUS SYSTEM AND METHOD FOR PERFORMING CROSS-ACCESS BETWEEN BUSES	KIM, JAE-HUN
10666507	Not Issued	071	09/19/2003	MOS TRANSISTOR HAVING A RECESSED GATE ELECTRODE AND FABRICATION METHOD THEREOF	KIM, JAE-HUN

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